

Biography

26 years in semiconductor manufacturing and technology deployment in Micron. Global working experiences with 20 years in Singapore (TECH Semiconductor, Intel Micron Flash Singapore and Micron Singapore), 3.5 years in Micron Japan Hiroshima, and Micron Taiwan since Nov 2021.

Take on roles with increasing responsibilities from Process & Equipment Engineer, Process Integration Engineer, Real-time Defects Analysis, Product Integration Engineering Director, ATE (Alpha Technology Engineering) Director, Senior Director of Advanced Technology Japan, High volume manufacturing (Micron Hiroshima), and currently VP of Taiwan Front End Manufacturing Operations.



Education:

- 1993-1997 National University of Singapore (NUS), B. Eng in Electrical Engineering

Experience:

- 2021 – Present: VP, Micron Taiwan FE MFG Operations
- 2018 – 2021: Senior Director, Micron Japan Hiroshima

-New Technology Deployment

-High Volume Manufacturing

- 1997 – 2017:

-Director, Product Integration Engineering (PIE), IMFS Singapore / Micron Singapore

-Process, PI, YE, Defect Analysis (DRAM), TECH Semiconductor