

Chia-Ming Yang received his BS degree in electric engineering from Chang Gung University, Tao-Yuan, Taiwan, in 1999; MS degree in electronic engineering from National Chiao Tung University, Hsin-Chu, Taiwan, in 2001; Ph.D degree in electronic engineering from Chang Gung University in 2006. He was a department manager in Inotera Technology Inc. for DRAM device and retention optimization from 2006 to 2012. He joined the Institute of Electro-Optical Engineering in Chang Gung University in 2012 as an assistant professor and is an associate professor since 2015. He became the institute director and a professor since Feb. and Aug. 2018. His research interests include DRAM retention and VRT, VLSI and MEMS technology, biomedical and chemical sensors, gas sensor and 2D material. He holds 4 U.S. and 22 Taiwan patents and is author of 79 SCI journal and 195 conference papers.

