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Vita Pi-Ho Hu is an Associate Professor in the Department of Electrical Engineering at National Taiwan University. She received her B.S. in Materials Science and Engineering and her Ph.D. in the Institute of Electronics from National Chiao Tung University in Hsinchu, Taiwan, in 2004 and 2011, respectively. Her research interests include Ferroelectric



FET, advanced nanoelectronics, low-power/high-performance SRAM, monolithic 3D IC, and design technology co-optimization. She has authored or co-authored over 100 refereed journal and conference papers about her research interests.

Dr. Hu received the Outstanding Paper Award at the 2011 IEEE International Conference on IC Design and Technology (ICICDT), the Postdoctoral Research Award from the Taiwan Semiconductor Industry Association (TSIA) in 2014, the Best Paper Award at the IEEE International Symposium on the Physical and Failure Analysis of Integrated Circuits (IPFA) in 2015, the Excellent Young Researcher Project Award from the Ministry of Science and Technology (MOST) in 2016, the Exploration Research Award from the Pan Wen Yuan Foundation in 2017, the Excellent Teaching Award of the EECS College in NCU in 2018, the MOST Young Scholar Fellowship in 2018, the Outstanding Youth Award from the Electronics Devices and Materials Association (EDMA) in 2019, the Ta-You Wu Memorial Award in 2021, and the Outstanding Women in Science Award in 2023. Before joining NTU, she served as an Assistant Professor in the Department of Electrical Engineering at National Central University from 2015 to 2019. Additionally, she was a Visiting Scholar in the Department of Electrical Engineering and Computer Science at the University of California, Berkeley, USA, from July to August 2017 and 2018. Since 2018, Dr. Hu has been actively involved as a technical committee member of ISQED. She also served as a committee member for IEDM in 2022-2023 and as a subcommittee chair in 2024.